

SANYO Semiconductors DATA SHEET

TIG030TS-

N-Channel IGBT

Light-Controlling Flash Applications

Features

- · Low-saturation voltage.
- · 4V drive.
- · Enhansment type.
- · Built-in gate-to-emitter protection diode.
- Mounting height 1.1mm, mounting area 19.2mm².
- · dv / dt guarantee.*

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Emitter Voltage	VCES		400	V
Gate-to-Emitter Voltage (DC)	VGES		±6	V
Gate-to-Emitter Voltage (Pulse)	VGES	PW≤1ms	±8	V
Collector Current (Pulse)	ICP	PW≤500μs, duty cycle≤0.5%, C _M =400μF	150	Α
Maximum Collector-to-Emitter dv / dt	dVCE / dt	VCE≤320V, starting Tch=25°C	400	V / μs
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-40 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =2mA, V _{GE} =0V	400			٧
Collector-to-Emitter Cutoff Current	ICES	VCE=320V, VGE=0V			10	μΑ
Gate-to-Emitter Leakage Current	IGES	V _{GE=±6} V, V _{CE=0} V			±10	μΑ

Marking: G030 Continued on next page.

- *: Conduct 100% screening of dv / dt (slope of collector voltage at the time of turn-off) by dv / dt>400V/µs.
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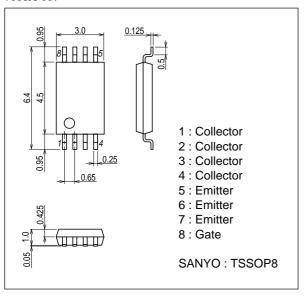
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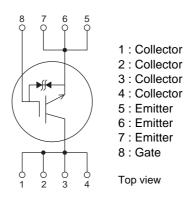
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Gate-to-Emitter Threshold Voltage	VGE(off)	VCE=10V, IC=1mA	0.5		1.2	V
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =150A, V _{GE} =4V		3.7	5.4	V
Input Capacitance	Cies	V _{CE} =10V, f=1MHz		2610		pF
Output Capacitance	Coes	VCE=10V, f=1MHz		59		pF
Reverse Transfer Capacitance	Cres	V _{CE} =10V, f=1MHz		36		pF

Package Dimensions

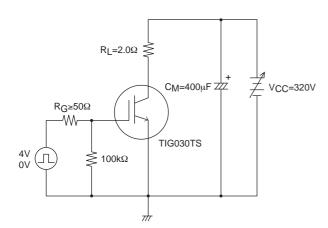
unit : mm (typ) 7006A-007



Electrical Connection

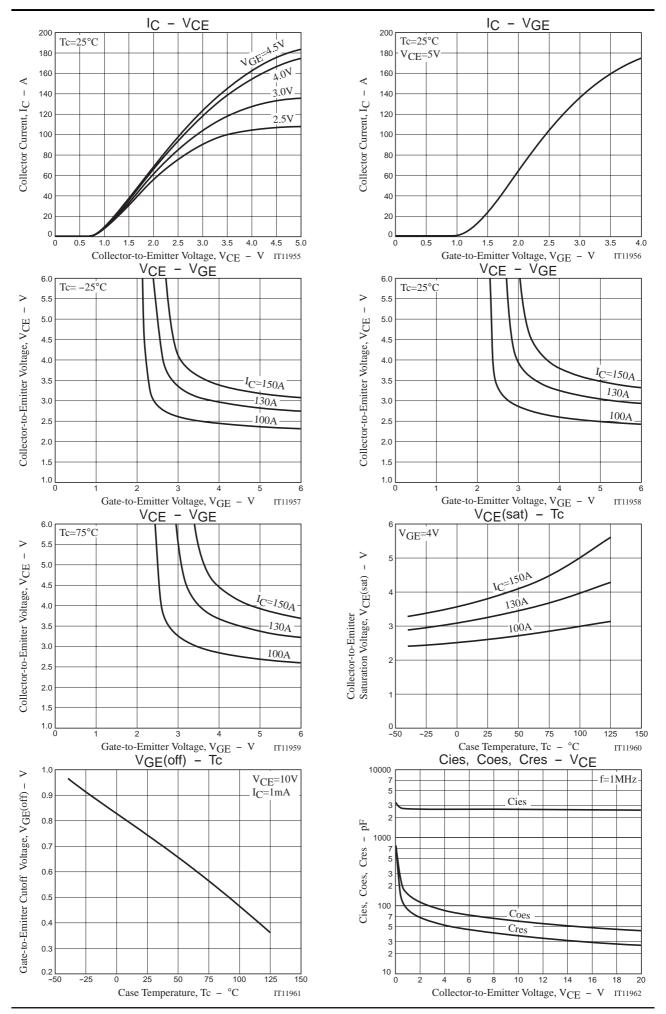


Large Current R Load Screening Circuit

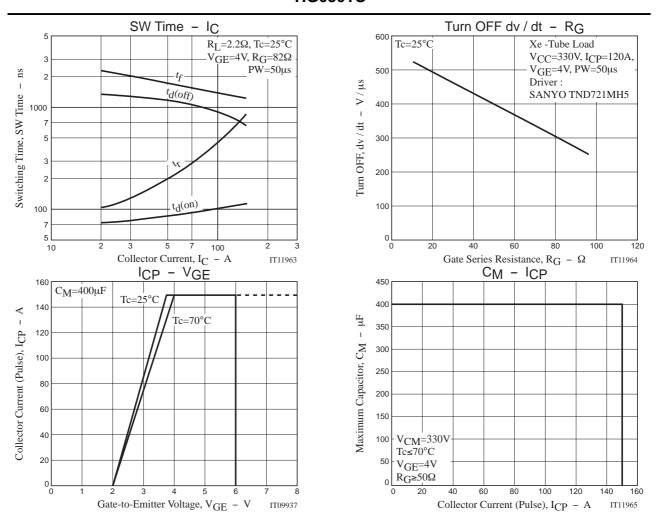


Note1. Gate Series Resistance $R_G \ge 50\Omega$ is recommended for prolection purpose at the time of turn OFF. However, if $dv/dt \le 400V/\mu s$ is satisfied at customer's actual set evaluation, $R_G < 50\Omega$ can also be used.

Note2. The collector voltage gradient dv / dt must be smaller than 400V / μs to protect the device when it is turned off.



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Note: TIG030TS has protection diode between gate and emitter but handling it requires sufficient care to be taken

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